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27296 7590 04/19/2007

LAWRENCE M. CHO
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Lawrence M. Cho	(Depositor's name)
<i>Lawrence M. Cho</i>	(Signature)
July 3, 2007	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/806,617	06/24/2004	Deshanand Singh	ALT.P023 (A1258)	2655

TITLE OF INVENTION: METHOD AND APPARATUS FOR PERFORMING LOGIC REPLICATION IN FIELD PROGRAMMABLE GATE ARRAYS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$0	\$1400	07/19/2007

EXAMINER	ART UNIT	CLASS-SUBCLASS
BOWERS, BRANDON	2825	716-016000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1. L. Cho
2. _____
3. _____

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(A) NAME OF ASSIGNEE

Altera Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

San Jose, California

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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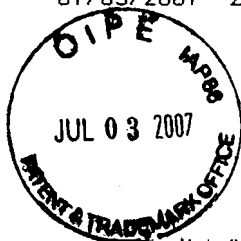
Authorized Signature *Lawrence M. Cho*
Typed or printed name Lawrence M. Cho

Date July 3, 2007

Registration No. 39,942

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39,942

Registration Number, if applicable

Fax: (217) 356-7373

Ph: (217) 377-2500

Telephone Number

Re: Serial No. 10/806,617

Attorney Docket No. ALT.P023 (A01258)

Filed: June 24, 2004

In re Application of: Deshanand P. Singh et al.

Title: Method and Apparatus for Performing Logic Replication in Field Programmable Gate Arrays

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